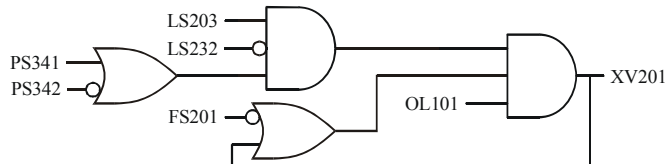


Errata

K. T. Erickson, *Programmable Logic Controllers: An Emphasis on Design and Application*, Second edition, July 2011.

- p. 66 Problem P2-2, 2nd line, "P325" should be "HN325"
 p. 70 Figure P2.8, the top input to the second gate should be "LS462", not "LS463".
 p. 70 Figure P2.9 should be,



- p. 85 In list of variable types, second "WORD" should be "DWORD", second "TIME" should be deleted, the range for REAL should be $\pm 1.18 \times 10^{-38}$ to $\pm 3.40 \times 10^{+38}$ (negative exponent sign missing).
 p. 90 Figure 3.5: Terminals on the modules should be numbered 1 to 4
 p. 94 In list of variable types, the range for REAL should be $\pm 8.43 \times 10^{-37}$ to $\pm 3.36 \times 10^{+38}$ (negative exponent sign missing).
 p. 122 In list of variable types, the range for REAL should be $\pm 1.175 \times 10^{-38}$ to $\pm 3.403 \times 10^{+38}$ (negative exponent sign missing); the range for LREAL should be $\pm 2.23 \times 10^{-308}$ to $\pm 1.79 \times 10^{+308}$ (exponent sign missing).
 p. 141 Figure 3.50: Terminals on the modules should be numbered 2 to 5
 p. 136 Table of data types, the range for REAL should be $\pm 1.175 \times 10^{-38}$ to $\pm 3.403 \times 10^{+38}$ (negative exponent sign missing); the range for LREAL should be $\pm 2.225 \times 10^{-308}$ to $\pm 1.79 \times 10^{+308}$ (negative exponent sign missing).
 p. 141-2 On GE systems, "rack" is a more appropriate term than "chassis".
 p. 238 Figure 5.38c, First falling edge of TIME_IN should line up with dashed line marking 1 second elapsed time.
 p. 243 Figure 5.45b, Q output on when CV=PV.
 p. 249 Figure 5.54, CV output available for PACSystems processors, also.
 p. 254 Figure 5.59, CV output available for PACSystems processors, also.
 p. 264 SPRAY Modicon address should be %Q0.3.6
 p. 273 CELL_25P Modicon address should be %Q0.3.8; CELL_ZRP Modicon address should be %Q0.3.9
 p. 284 Problem P5-7 statement, 6th line, "...PROX727 is ignored if CYL721 is off." should be "...PROX727 is ignored if **CYL725** is off." (correction in bold type)
 p. 291 Problem P5-15, 4th paragraph, last line, "P7-21" should be "P5-21". Last line of page, "plate" should be "pin."
 p. 298 Problem P5-21, in description of BIN_PROX, "plate" should be "pin."
 p. 384 In list of data types or addresses of internal variables, "GE" should be above last "Addr" column.

- p. 421 In list of variables, HEAT_DLS margin not correct for second line.
- p. 423 In list of addresses, delete extra space before underline in "FBLOW_ON", "FVAC_ON", and "PVAC_ON".
- p. 434 Second paragraph, 10th line. The reset switch should be ignored if the station is running and if the heated knife is moving or being held down.
- p. 488 Line above Figure 7.60. DWORD type also supported by PACSystems processors.
- p. 510 LT104_MEAS Modicon address should be "%IW0.4.0"
- p. 529 First line: "P7-32" should be "P7-29"
- p. 539 In list of addresses of internal variables, "FT723_VAL" listed twice. Delete the second one.
- p. 542 In list of variables, PT105_HI_LA should be **on** when measured pressure > 150% of set **maximum** desired pressure; For PT105_LO_HMI and PT105_HI_HMI, delete "thumbwheel switch reading". In list of addresses, the Modicon address for XV105_SOL should be %Q0.2.1, for PT105_LO_LA, it should be %Q0.2.2, for PT105_HI_LA, it should be %Q0.2.3, for PT105_HORN, it should be %Q0.2.4.
- p. 547 About middle of page: "operaion" should be "operation"
- p. 551 Seventh major step should be "Add water until the solution pH is 9 ± 0.3 ($8.7 \leq \text{pH} \leq 9.3$)"
- p. 555 Third line, "TANK_VAL" should be "TANK_LVL"
- p. 559 Sixth line, "VAC_MMHG" should be "VACP_MMHG"
- p. 643 Figure 8.67. Coils should be circles, not parentheses.
- p. 653 Figure 8.75. Coils should be circles, not parentheses.
- p. 654 Figure 8.77. Coils should be circles, not parentheses.
- p. 662 Figure 8.82, rung 3, File should be "#B12:0" and Bit Address should be "B3/95"
- p. 697 Step_Num type should be INT
- p. 731-3 Figure 9.16, rungs 3 and 21, "/DN" as reference to timer done bit should be ".DN" (5 instances).
- p. 733 Figure 9.16, comment at top of page: "16 – 23" should be "23 – 30"
- p. 735 Figure 9.17, comment at bottom of page: "16 – 23" should be "23 – 30"
- p. 740 Type of IStep for Siemens should be WORD
- p. 753 Figure 9.29, In_Mask for step 12 should be 16#0005, Seq. In Bits step 8 "x" for Eng1_Tmr done should be deleted.
- p. 754 Figure 9.29, Seq_OutAry for step 2 should be 16#0101
- p. 850 addresses of physical inputs, Siemens address of FT2502 should be IW514
- p. 852 Figure 10.77, second rung, input to INT_TO_REAL should be "FT2502"; Second block of parameters, for "FIC2501_PA" should be for "FIC2502_PA"
- p. 853 Figure 10.78; first contact for rung 61 should be "TIC2501.SWM".
- p. 854 Figure 10.79; Rung 43 PID block: Change the Control Variable field to "N7:10". The Control Variable field must be a word address. It is not necessary to use a MOV block to transfer to PD19:2.SP. It is handled automatically by the cascade configuration.
- p. 855 Figure 10.79: Add rung with a NO contact referring to HEX_TEMP controlling a coil referring to PD19:2/CL, which "breaks" the cascade loop when only steam flow is to be controlled.

- p. 874 Address list for P10-16: "FY1501" should be "FY1502" and "FY1502" should be "FY1503."
- p. 876 Variable list: Delete extra space in "FIC1402_SP" and "FIC1403_SP"
- p. 876 Variable list for P10-18: Delete extra space in "FIC1402_SP" and "FIC1403_SP"
- p. 880 Figure 11.1: Placing a NC contact referring to Tic_Ctr.Q in series with the other contacts connected to the IN input to Tic_Tmr will stop the counter from incrementing beyond PV.
- p. 921 Figure 11.40, second network, input to INT_TO_REAL should be "FT2502"; Second block of parameters, for "FIC2501_PA" should be for "FIC2502_PA"
- p. 923 BNOT at bottom of figure should have a tag of "BNOT_02", SPCascade input to PIDE should have an "assume data available" symbol.
- p. 953 Figure 12.5a, square beside "Timer" should be absent.
- p. 964 PLC-5 solution to Example 12.3, comments should start with "///". Second line, division operator should be "|"
- p. 964-5 Example 12.3, all instances of "LT428_RAW" should be "LT428_MEAS"
- P. 966 ControlLogix solution to Example 12.4, 5th line, "T428_MINDP" should be "LT428_MINDP"
- p. 967 PLC-5 solution to Example 12.4: comments should start with "///". On line 5, "T428_MINDP" should be "LT428_MINDP". On line 15, "AND" should be after "T428_HRN_ACT"
- p. 968 Second line after BEGIN, "LEVEL_DISP" should be "LT428_DISP"
- p. 969 First full paragraph, 2nd line, "Figure 12.8" should be "Figure 11.33"
- p. 987 Example 13.3 Modicon solution, line 5, "LT428_RAW" should be "LT428_MEAS"
- p. 987 Example 13.3 S7 solution, line 2, "LT428_RAW" should be "LT428_MEAS"
- p. 988 Example 13.3 GE solution, line 2, "LT428_RAW" should be "LT428_MEAS"
- p. 989 S7-STL solution, 6th line, LEVEL_DISP" should be "LT428_DISP"
- p. 914 Figure 11.37, last rung on page "Mtr_Fail_Tmr" should be "Fail_Tmr"
- p. 990 GE solution to Example 13.4, fifth line, "LT_DISP" should be "LEVEL_DISP"
- p. 1020 Third line, "RESeSt" should be "RESet"
- p. 1080 Figure 14.80: Rectangle above S3 actions should be "Raise_Rotator3", Rectangle above S4 actions should be "Rotate_CCW2"
- p. 1081 Figure 14.81: Rung 4: "InvertMainDB" should be "InvertNormalDB" and "Invert_Main" should be "Invert_Normal", INIT_SQ input should be "Int_Reset".
- p. 1247 Figure 17.47 in rung 2 comment, "101.126.43.200" should be "192.168.0.164"
- p. 1381 In list of physical inputs and outputs and in addresses, "XV102_Sol_Vlv" should be "XV102_SolVlv"
- p. 1406 Figure 21.40, first rung, bottom input to DIV_DI block should be L#1000
- p. 1412 Figure 21.42, rung 48 variable above first contact should be "Xfer_Shutdown.Step[2]"; Figure 21.43, rung 1, extra "." in IN1 input to NE INT block